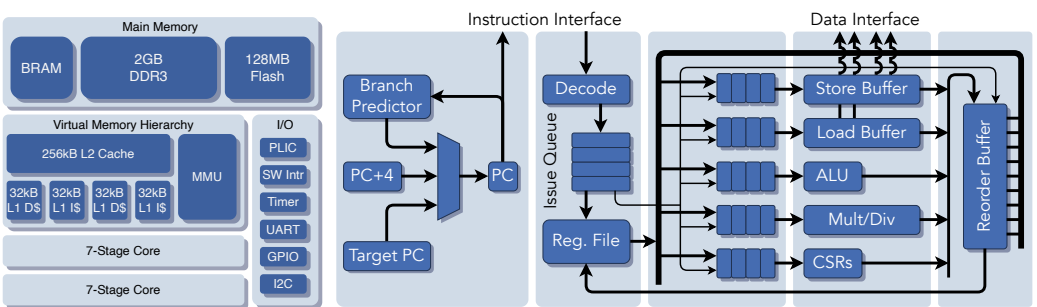


Fast, effective, and secure processing needs versus processing power availability asymmetry



- Compute Capabilities bridge semantic gaps between layers**
  - Security constructs preserved between system layers
  - Supported with end-to-end system design
- Hardware Root-of-Trust enforces security policies defined at higher abstractions**
  - Architectural support for security
  - With authentication, anti-tamper, and encryption support
- Security baked into all levels of the computing stack**
  - Application, Compiler, Operating System, and Architecture
  - Formally-Verified for high-assurance

- Our solution is a spectrum computing architecture that seamlessly scales from the periphery to the central data repository.
- A single instruction set and architecture that attach to computational elements (such as memory/data, computational nodes, and peripheral devices) specific metadata detailing access rights and usage rules enforced at the hardware level.
- The SASCA Architecture facilitates the seamless communication of data, ensuring its protection and security as it moves from the edge to the cloud and vice versa, all while maintaining strict adherence to access guidelines wherever the data is employed or retrieved.



This is a truly innovative solution to geographically distributed computing. Contact us to learn more.

[www.securemicro.com](http://www.securemicro.com)